

MOSFETs Silicon N-Channel MOS (U-MOSVI-H)

TPC6008-H

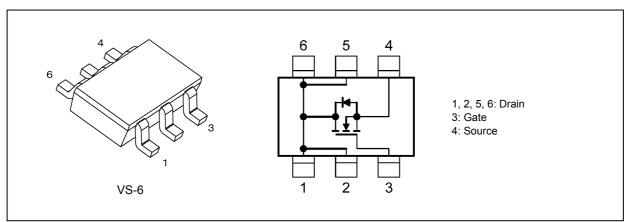
1. Applications

- · High-Efficiency DC-DC Converters
- · Notebook PCs
- · Mobile Handsets

2. Features

- (1) Small, thin package
- (2) High-speed switching
- (3) Small gate charge: $Q_{SW} = 0.9 \text{ nC (typ.)}$
- (4) Low drain-source on-resistance: $R_{DS(ON)} = 50 \text{ m}\Omega$ (typ.) ($V_{GS} = 4.5 \text{ V}$)
- (5) Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{ V)}$
- (6) Enhancement mode: $V_{th} = 1.3 \text{ to } 2.3 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 0.1 \text{ mA})$

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (T_a = 25°C unless otherwise specified)

Characteris	Symbol	Rating	Unit		
Drain-source voltage			V_{DSS}	30	V
Gate-source voltage			V _{GSS}	±20	1
Drain current (DC)		(Note 1)	I _D	5.9	Α
Drain current (pulsed)		(Note 1)	I _{DP}	23.6	1
Power dissipation	(t = 5 s)	(Note 2)	P _D	2.2	W
Power dissipation	(t = 5 s)	(Note 3)	P _D	0.7	W
Single-pulse avalanche energy		(Note 4)	E _{AS}	9.05	mJ
Avalanche current			I _{AR}	5.9	Α
Channel temperature			T _{ch}	150	°C
Storage temperature			T _{stg}	-55 to 150	1

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production



5. Thermal Characteristics

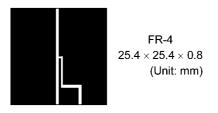
Characteristics				Max	Unit
Channel-to-ambient thermal resistance	(t = 5 s)	(Note 2)	R _{th(ch-a)}	56.8	°C/W
Channel-to-ambient thermal resistance	(t = 5 s)	(Note 3)	R _{th(ch-a)}	178	°C/W

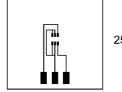
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 3: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 4: V_{DD} = 24 V, T_{ch} = 25°C (initial), L = 0.2 mH, R_G = 1 Ω , I_{AR} = 5.9 A





 $\begin{aligned} & \text{FR-4} \\ 25.4 \times 25.4 \times 0.8 \\ & \text{(Unit: mm)} \end{aligned}$

Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)

Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.



6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±0.1	μΑ
Drain cut-off current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	30			V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	15			
Gate threshold voltage	V_{th}	V _{DS} = 10 V, I _D = 0.1 mA	1.3		2.3	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 4.5 V, I _D = 3 A	_	50	74	mΩ
		V _{GS} = 10 V, I _D = 3 A	_	40	60	

6.2. Dynamic Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	232	300	pF
Reverse transfer capacitance	C _{rss}			16	26	
Output capacitance	C _{oss}			68	_	
Gate resistance	r _g	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 5 \text{ MHz}$		5	6.5	Ω
Switching time (rise time)	t _r	See Figure 6.2.1.		2.4	_	ns
Switching time (turn-on time)	t _{on}			7.4	_	
Switching time (fall time)	t _f		_	2.0	_	
Switching time (turn-off time)	t _{off}		_	11	_	

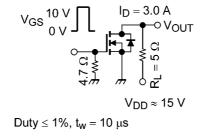


Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	Q_g	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5.9 \text{ A}$		4.8	1	nC
gate-drain)		$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 5.9 \text{ A}$		2.6		
Gate-source charge 1	Q _{gs1}	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5.9 \text{ A}$	_	1.3		
Gate-drain charge	Q_{gd}		_	0.8	_	
Gate switch charge	Q_SW			0.9		

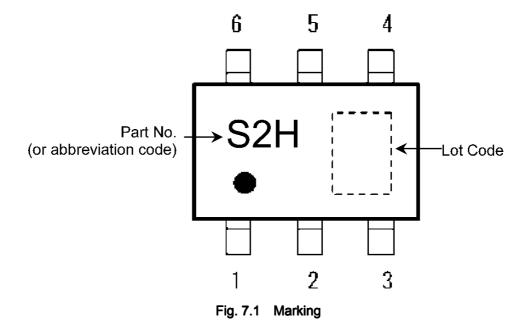
6.4. Source-Drain Characteristics (T_a = 25°C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed) (N	Note 5)	I _{DRP}	_	_	_	23.6	Α
Diode forward voltage		V_{DSF}	I _{DR} = 5.9 A, V _{GS} = 0 V		_	-1.2	V

Note 5: Ensure that the channel temperature does not exceed 150°C.



7. Marking



8. Characteristics Curves (Note)

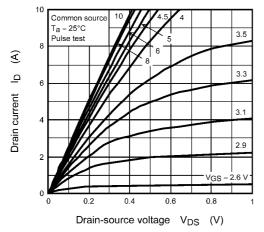
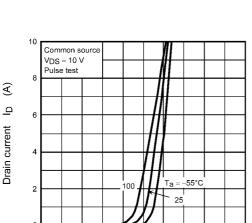


Fig. 8.1 I_D - V_{DS}



Gate-source voltage V_{GS} (V) Fig. 8.3 I_D - V_{GS}

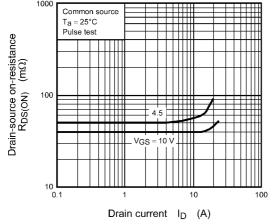


Fig. 8.5 R_{DS(ON)} - I_D

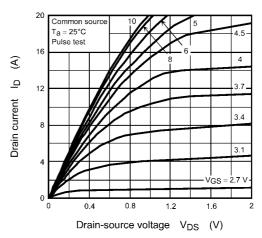


Fig. 8.2 I_D - V_{DS}

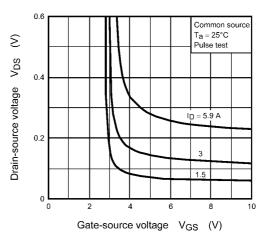


Fig. 8.4 V_{DS} - V_{GS}

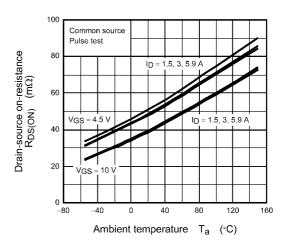


Fig. 8.6 R_{DS(ON)} - T_a

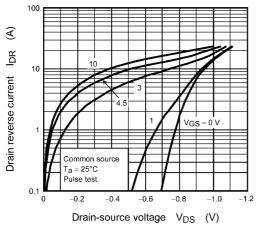


Fig. 8.7 IDR - VDS

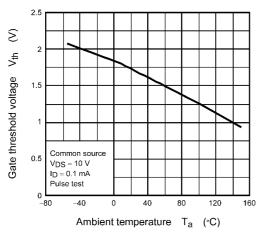


Fig. 8.9 Vth - Ta

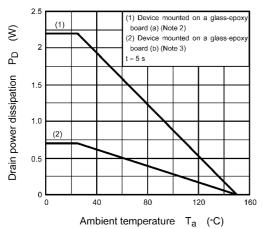


Fig. 8.11 P_D - T_a (Guaranteed Maximum)

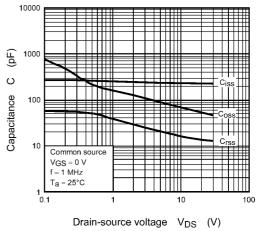


Fig. 8.8 Capacitance - V_{DS}

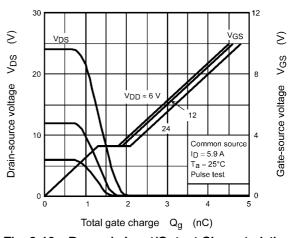


Fig. 8.10 Dynamic Input/Output Characteristics

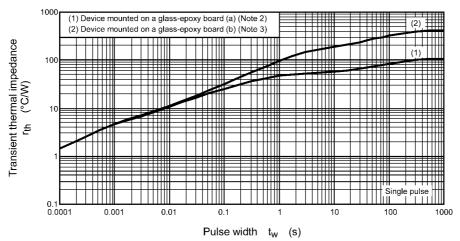


Fig. 8.12 r_{th} - t_w (Guaranteed Maximum)

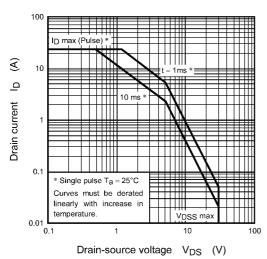


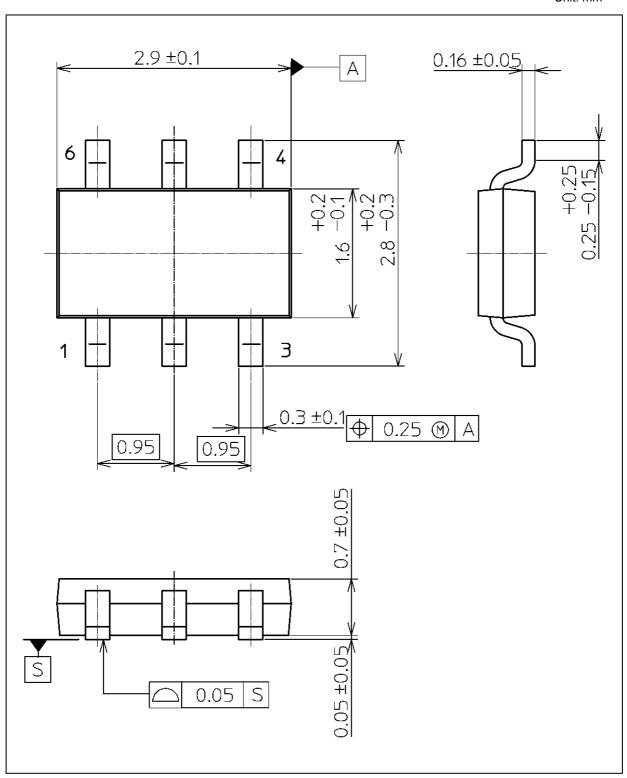
Fig. 8.13 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.011 g (typ.)

	Package Name(s)
TOSHIBA: 2-3T1S	
Nickname: VS-6	



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